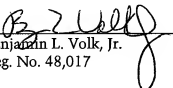


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF E-FILEING

I hereby certify that this correspondence has been e-filed with the U.S. Patent and Trademark Office on February 23, 2011.


Benjamin L. Volk, Jr.
Reg. No. 48,017

In re application of: Chamberlain et al.	:	
	:	
Serial No.: 10/550,323	:	Examiner: Fleurantin, Jean B.
	:	
Filed: September 1, 2006	:	Group Art Unit: 2162
	:	
For: Intelligent Data Storage and Processing	:	
Using FPGA Devices	:	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Amendment After Final Following Examiner Interview

In response to the Office Action dated January 3, 2011, and following Applicant's interview with the Examiner on February 18, 2011, Applicant submits the following amendment and remarks.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 26 of this paper.